

WHAT IS CLAIMED IS:

1. A network device, comprising:

a first port to allow the device to communicate with other devices on an expansion bus;

5 a second port to allow the device to communicate with devices on a second bus;

a memory to store data; and

a processor to:

receive an interrupt signal from an expansion device on the expansion bus;

generate an indicator of completion; and

10 insert the indicator into a transaction queue after the set of data.

2. The network device of claim 1, the network device further comprising a peripheral component interconnect bridge.

3. The network device of claim 1, the second bus being a system bus.

4. The network device of claim 1, the second bus being an expansion bus.

15 5. The network device of claim 1, the processor to generate an indicator of completion further comprising the processor to generate a transaction addressed to a predetermined area of a system memory.

6. The network device of claim 1, the processor to generate an indicator further comprising the processor to issue a read request to the expansion device.

20 7. The network device of claim 6, the processor to insert the indicator further comprising the processor to transmit data from the read request to a predetermined address in a system memory.

8. A method, comprising:

receiving an interrupt signal from an expansion device on an expansion bus indicating

25 a data transfer is complete;

inserting a set of data from the data transfer into a transaction queue;

generating an indicator of completion; and

inserting the indicator into the transaction queue after the set of data.

9. The method of claim 8, generating an indicator of completion further comprising generating a transaction addressed to a predetermined area of a system memory.

5 10. The method of claim 9, inserting the indicator further comprising inserting the transaction into the transaction queue.

11. The method of claim 8, generating an indicator of completion further comprising generating a read request to the expansion device.

12. The method of claim 11, inserting the indicator into the transaction queue further
10 comprising inserting data from the read request into the transaction queue, addressed to a predetermined area of a system memory.

13. The method of claim 8, the method further comprising:

receiving data from network device;

receiving the indicator at a predetermined area of memory;

15 generating an interrupt to a system processor in response to the indicator; and processing the data from the network device.

14. A network device, comprising:

a means for allowing the device to communicate with other devices on an expansion bus;

20 a means for allowing the device to communicate with devices on a second bus;

a means for storing data; and

a means for:

receiving an interrupt signal from an expansion device on the expansion bus;

generating an indicator of completion; and

25 inserting the indicator into a transaction queue after the set of data.

15. The network device of claim 1, the network device further comprising a peripheral component interconnect bridge.

16. The network device of claim 1, the means for allowing the device to communicate with device on a second bus further comprising a means to allow the device to communicate
5 on a system bus.

17. The network device of claim 1, the means for allowing the device to communicate with device on a second bus further comprising a means to allow the device to communicate
on a second expansion bus.

18. An article of machine-readable media containing instructions that, when executed, cause
10 the machine to:

receive an interrupt signal from an expansion device on an expansion bus indicating a data transfer is complete;

insert a set of data from the data transfer into a transaction queue;

generate an indicator of completion; and

15 insert the indicator into the transaction queue after the set of data.

19. The article of claim 18, the instructions causing the machine to generate an indicator of completion further cause the machine to generate a transaction addressed to a predetermined area of a system memory.

20. The article of claim 19, the instructions causing the machine to insert the indicator further
20 causing the machine to insert the transaction into the transaction queue.

21. The article of claim 18, the instructions causing the machine to generate an indicator of completion further causing the machine to generate a read request to the expansion device.

22. The article of claim 21, the instructions causing the machine to insert the indicator into
25 the transaction queue further causing the machine to insert data from the read request into the transaction queue, addressed to a predetermined area of a system memory.